

ABSTRACT OF THE DISCLOSURE

An apparatus is provided for automatically modifying serious semantic grammar errors in an HDL description and to clearly show the modified portions. For these purposes, the apparatus includes: means for detecting a portion in which variables on the right and the left sides of an assignment statement are inconsistent in type; a template for converting the type of the variable on the right side of the assignment statement into that of the variable on the left side; means for modifying the portion into a correct description by applying the type conversion function to the right side of the assignment statement having the portion; and means for attaching a comment about the modification to the modified portion, and is used to automatically modify inappropriate descriptions in the design information of an electronic system or of a logic circuit described in a hardware description language such as VHDL and Verilog-HDL.